

FIGURE 1

Synchronous Collapsed Ring Architecture for Real-Time Signal Switching and Processing

Distribution
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Attorney's Docket: 064751.0312
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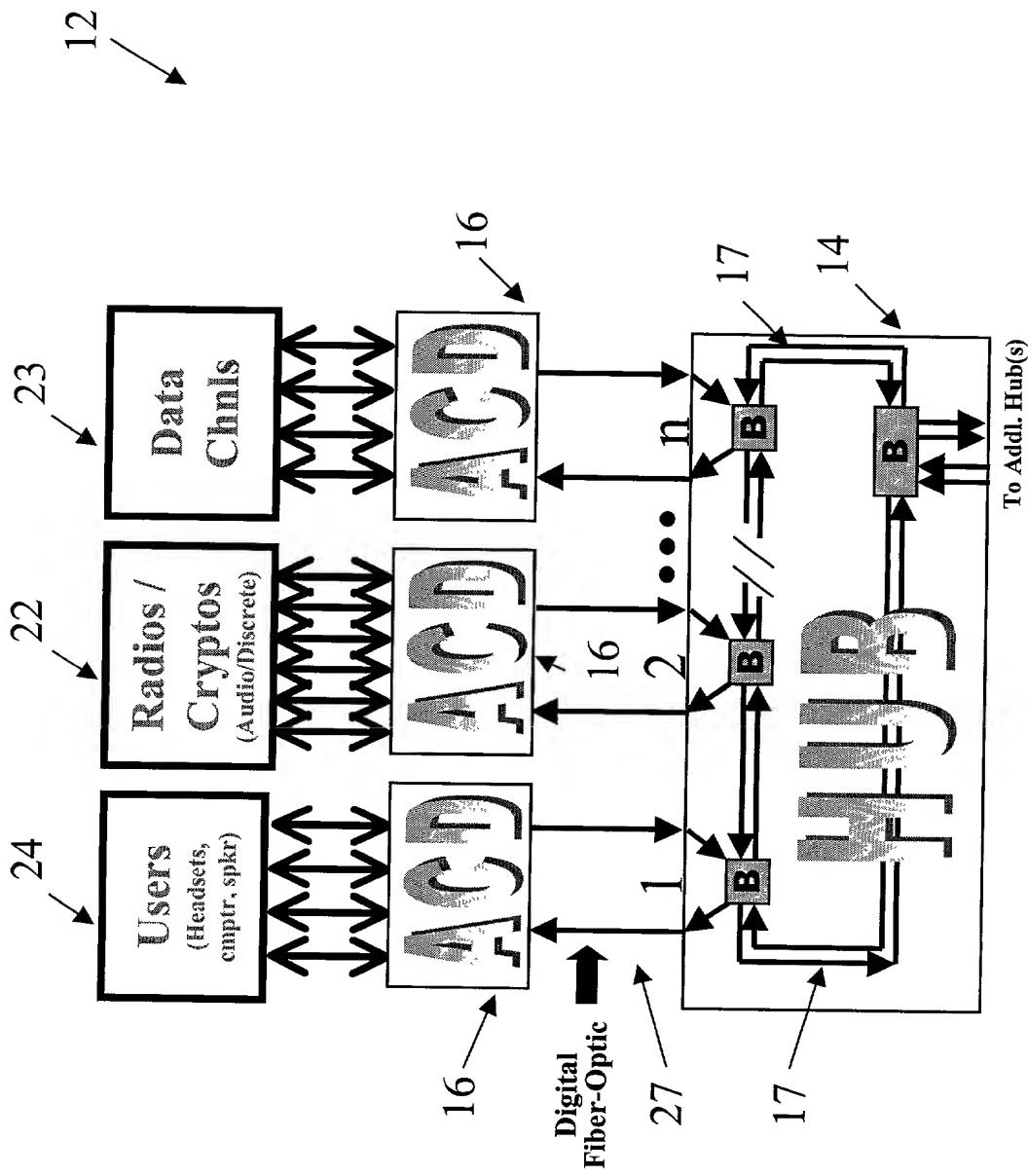


FIGURE 2A

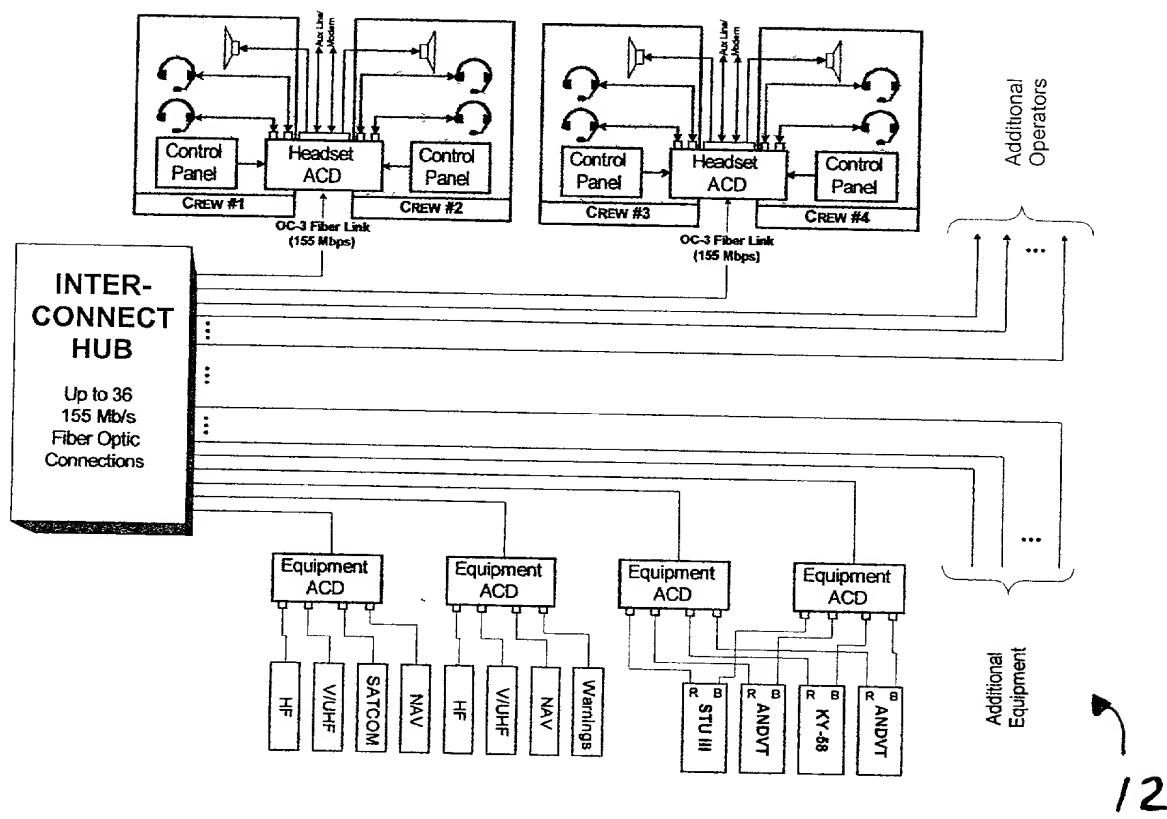


FIGURE 2B

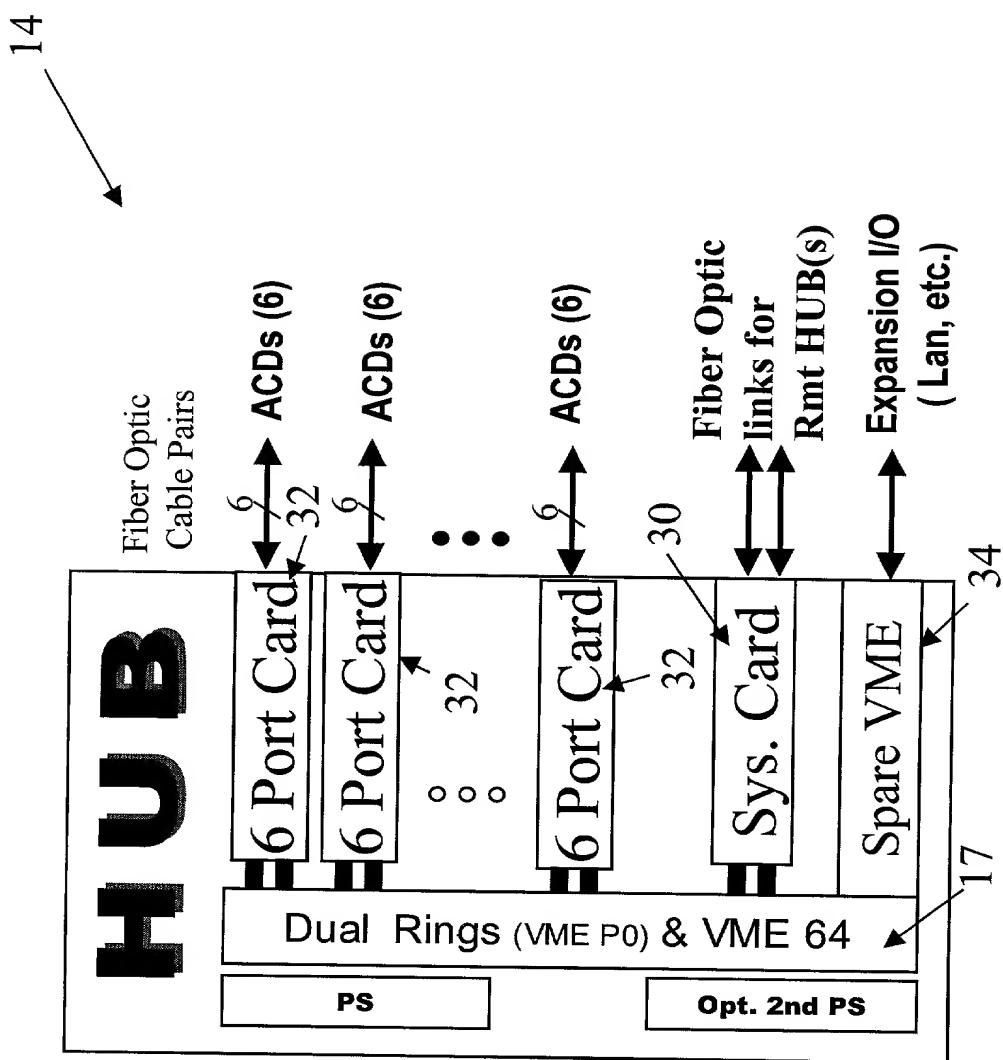


FIGURE 3A

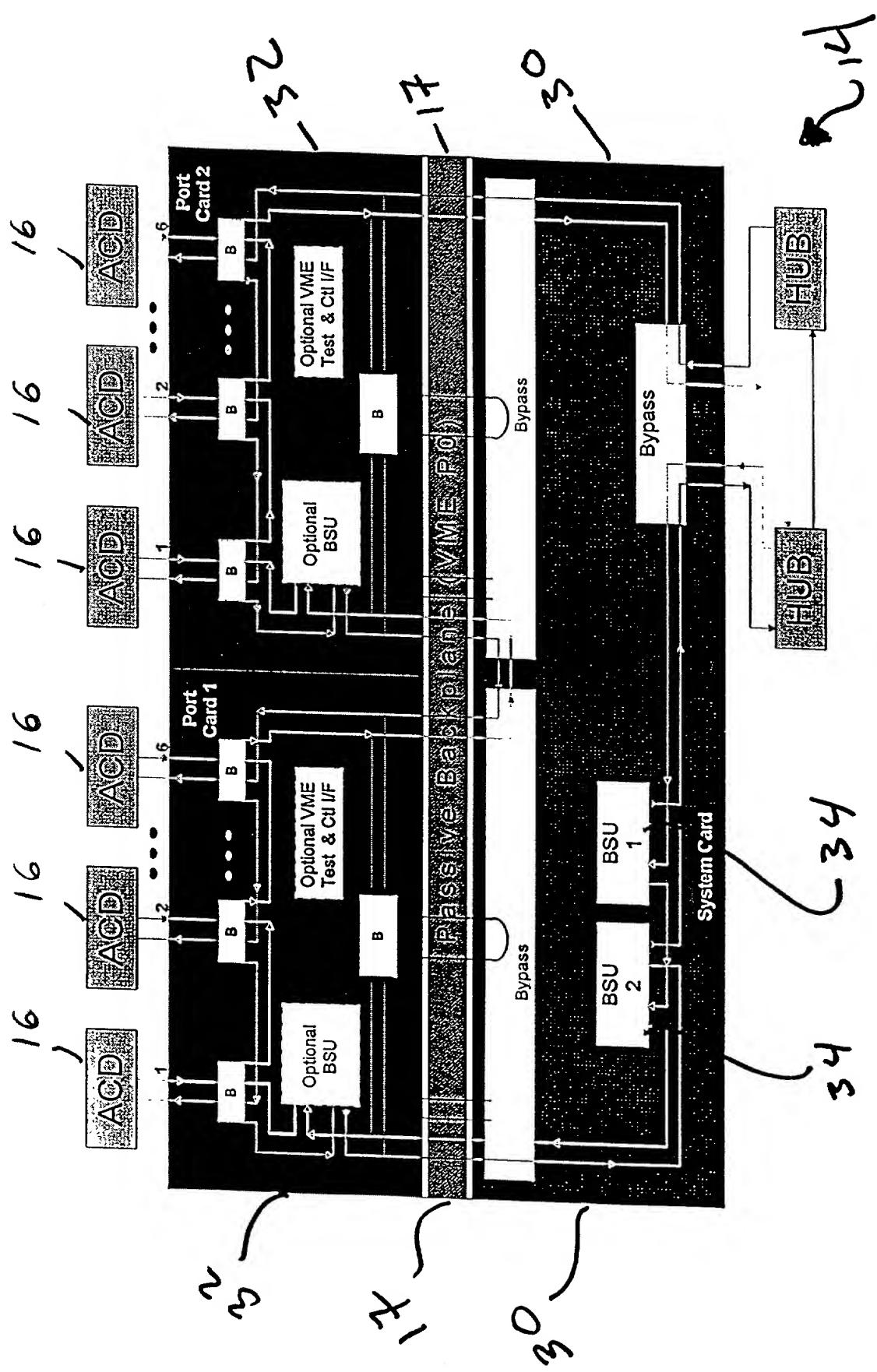


FIGURE 3B

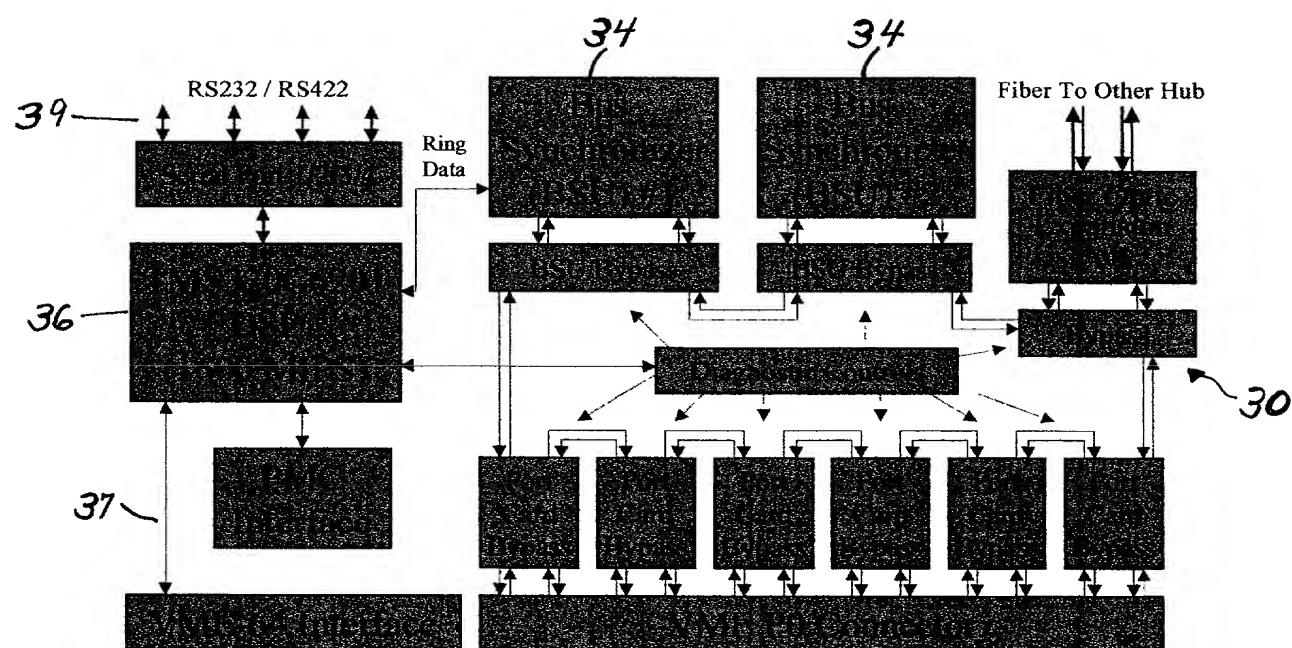


FIGURE 4

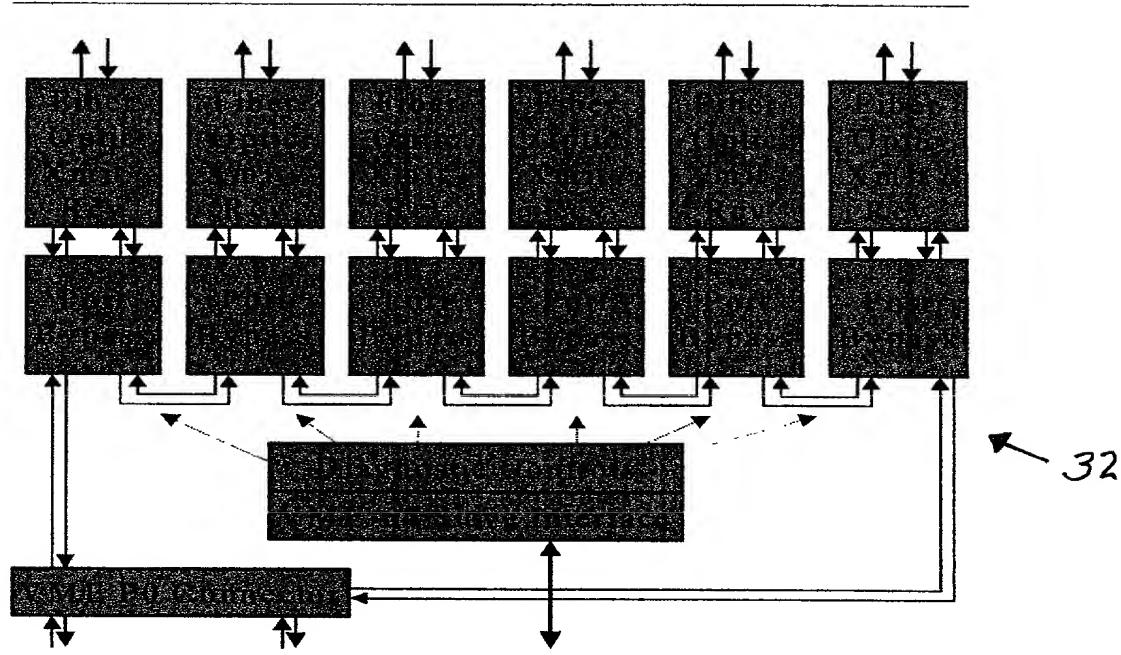


FIGURE 5A

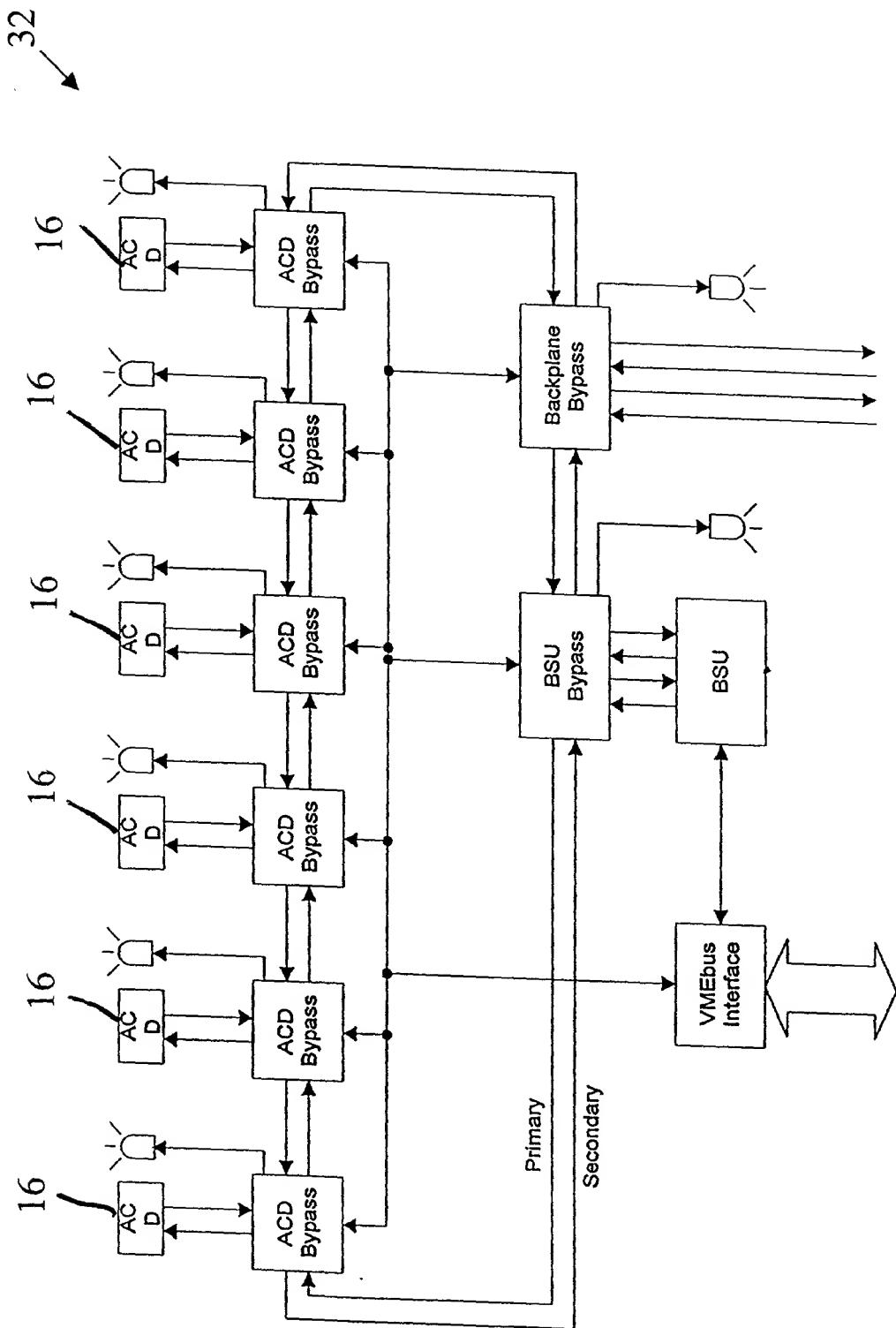


FIGURE 5B

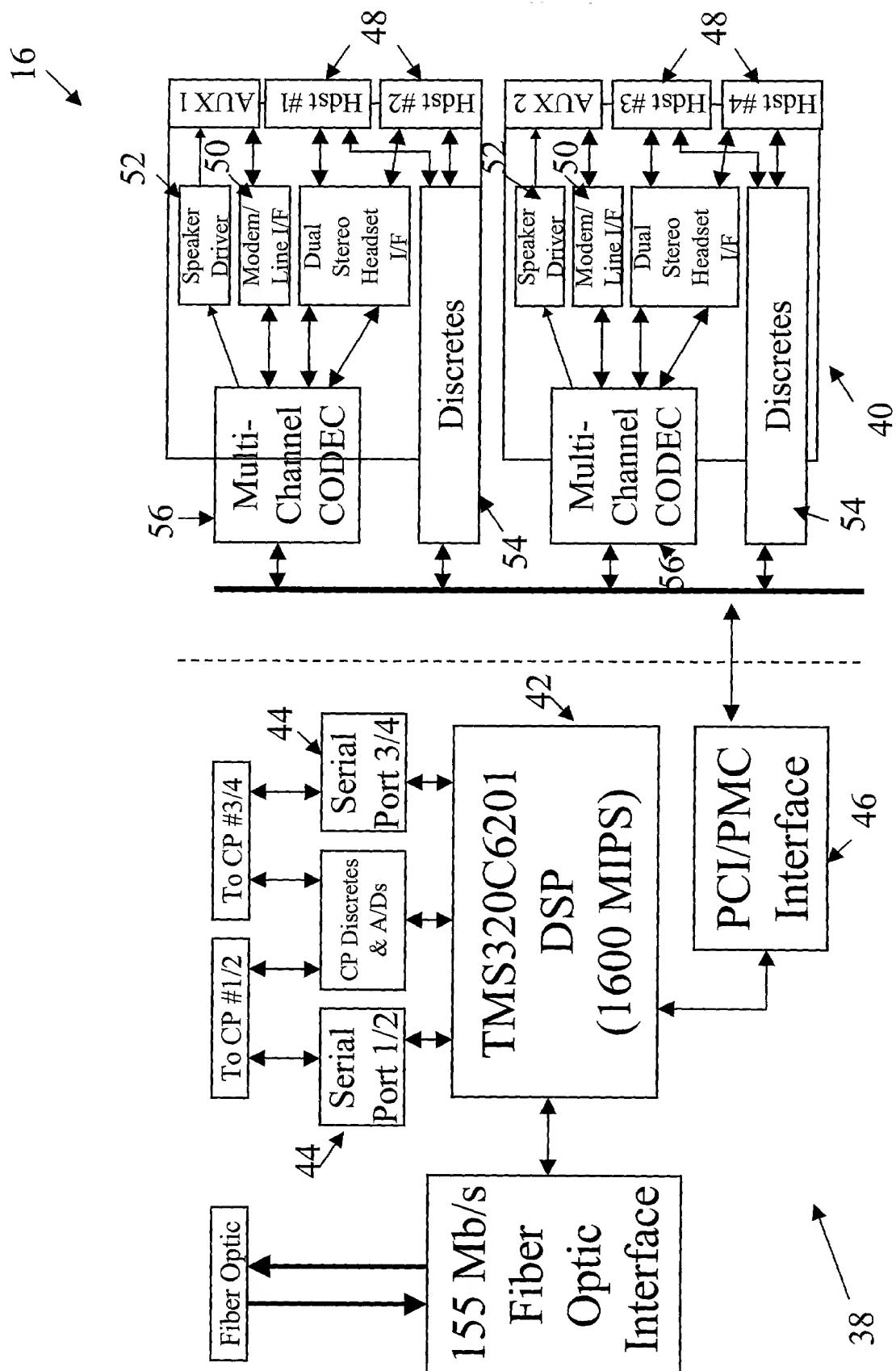


FIGURE 6